



पूर्वोत्तर क्षेत्रीय विज्ञान एवम् प्रौद्योगिकी संस्थान North Eastern Regional Institute of Science & Technology

(Deemed to be University u/s 3 of the UGC Act, 1956)
Under the Ministry of Education, Govt. of India

निर्जुली - ७९१ १०९ (ईटानगर)
अरुणाचल प्रदेश, भारत

Nirjuli - 791 109 (Itanagar)
Arunachal Pradesh, India

DEPARTMENT OF ELECTRICAL ENGINEERING NERIST, NIRJULI, A.P-791109

Date: 08/07/2024

Walk in Interview for Guest Faculty

Department of Electrical Engineering, NERIST is looking for Guest Faculty. Interested candidates with bright academic career may register through link provided below and appear before the selection committee along with a brief bio-data and all supporting documents as per the schedule given below:

Schedule for the Walk in Interview

Sl. No.	Department	No. of Posts	Last date for online application	Date and time of Interview	Venue	Minimum Qualification
1.	Electrical Engineering	03	25/07/2024 5.00 PM (IST)	26/07/2024 10.00AM (IST)	Department of Electrical Engineering	M.Tech/M.E in Electrical Engineering

Link to submit the Application: <https://forms.office.com/r/Tnuy2gkuKt>

Qualification: As per UGC/AICTE Norms.

Emoluments: Appointed Guest Faculties will be paid Rs.1500.00 per lecture subject to a maximum of Rs. 50,000.00 per month. No other allowances will be admissible.

NOTES:

1. Applicants must report at the venue by 9:30AM on 26/07/2024.
2. Applicants are requested to bring all supporting documents in original for verification at the time of interview along with self-attested photo copy of the same.
3. The selections shall be based only on the performance in the interview.
4. With regard to minimum requirement of educational qualification, screening, etc., relevant regulation of UGC/AICTE will be applicable.
5. Applications received after the last date of registration, applications with incomplete data will be rejected.
6. The appointment shall be purely on temporary basis for a fixed period not exceeding the duration of the semester for which the appointment is being made.
7. No TA/DA shall be provided to the applicants for attending the Interview.

Head
Department of Electrical Engineering