



# NORTH EASTERN REGIONAL INSTITUTE OF SCIENCE & TECHNOLOGY

(Deemed-to-be-University)

[Under the Ministry of Education, Govt. of India, New Delhi]

Nirjuli :: Itanagar :: Arunachal Pradesh :: 791109, India

No. VisvesvarayaPhD(Phase-II)/ECE/2025-01

Dated, Nirjuli the 26<sup>th</sup> May 2025

## ADMISSION NOTIFICATION

### **ADVERTISEMENT FOR VISVESVARAYA PHD SCHEME PHASE-II ADMISSIONS BOTH PH.D FULL TIME AND PH.D SPONSORED (PART-TIME)**

Digital India Corporation, MeitY (Ministry of Electronics and Information Technology), Govt. of India has granted scholarships under the scheme titled “Visvesvaraya PhD scheme for Electronics and IT: Phase-II”. Under this scheme presently admissions are open to Full Time and Part Time PhD program in the Department of Electronics and Communication Engineering (ECE)/ Computer Science and Engineering (CSE). This is a special scheme of Govt. of India with following features:

#### **Salient features for Full Time Scholars:**

1. **Fellowship:** Rs. 38,750 per month in 1<sup>st</sup> & 2<sup>nd</sup> year and @Rs. 43,750 per month in 3<sup>rd</sup>, 4<sup>th</sup> and 5<sup>th</sup> years of PhD. (support till PhD completion or 5 years whichever is earlier).
2. **Reimbursement of Rent (RoR):** This component is linked with the fellowship of PhD Candidate. The rate of RoR is 24%, 16% & 8% (of fellowship) for X, Y & Z class cities/towns respectively. The classification of the cities is as per the notification issued by the Ministry of Finance for the reimbursement of HRA.
3. **Institutional Overheads:** An amount of Rs. 25,000/Year/Full-time PhD candidate for support duration of PhD candidate to be given to the institution.
4. **Research Contingency Grant Support:** An amount of Rs. 1,20,000/Year/Full-time PhD candidate for support duration of PhD candidate.
5. **Support for attending International Conference:** Support upto Rs. 1.5 Lakhs/Full-time PhD candidate. This would cover the travel and other expenses of PhD candidate for attending International conferences, where his/her research paper has been accepted for the presentation by him/her.
6. **Visit to Labs abroad:** A support for each selected Full Time PhD candidate upto Rs. 10.5 Lakhs for visit to labs abroad.

#### **Salient features Sponsored (Part Time) Scholars:**

1. Candidate is eligible to receive a One-time incentive of Rs. 3.00 Lakhs on successful completion of PhD.

Applications are invited from Indian nationals for admission into the PhD Program in the area of Electronic System Design and Manufacturing (ESDM)/IT/IT enabled services (ITES) for the odd semester 2025-26, under the above scheme at North Eastern Regional Institute of Science and Technology, Nirjuli.

| Sl. No. | Department                                | Eligibility Criteria   | Specialization                                       |
|---------|---|--|--|
| 1       | Electronics and Communication Engineering | i) Candidates with two-year Postgraduate in Engineering / Technology in ECE/CSE/ | Semiconductor Devices, Biosensors and VLSI Circuits, |



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|   |   |   |  |
|---|---|---|--|
|   | <b>(ECE)/<br/>Computer<br/>Science and<br/>Engineering<br/>(CSE) [Full<br/>Time Category]</b>   | <p>IT/ or an allied area having 60% or 6.5 CGPA throughout his/her academic career.</p> <p>ii) Preference shall be given to GATE / NET qualified candidates.</p>  | Digital Design, Sensor and Circuits, MEMS, IoT, GaN and SiC based device, Electronic Sensor Design and Development, Bio-Electronics, Communication System, Signal processing, Biomedical Signal and Image Processing, Machine Learning, Wireless Communication, Massive MIMO, WSN, High Power Microwave Devices  |
| 2 | <b>Electronics and<br/>Communication<br/>Engineering<br/>(ECE)/<br/>Computer<br/>Science and<br/>Engineering<br/>(CSE)<br/>[Sponsored (Part<br/>Time) Category]</b> | <p>i) Candidates with two-year Postgraduate in Engineering / Technology in ECE/CSE/ IT/ or an allied area having 60% or 6.5 CGPA throughout his/her academic career.</p> <p>ii) Preference shall be given to GATE / NET qualified candidates.</p> <p>iii) Employees from Industries and Research &amp; Development (R&amp;D) Organizations, Employees from Academic Institutes and Faculties of Senior Colleges (Junior college are not to be included) with academic degrees as mentioned in point no. (i) and (ii).</p> | Semiconductor Devices, Biosensors and VLSI Circuits, Digital Design, Sensor and Circuits, MEMS, IoT, GaN and SiC based device, Electronic Sensor Design and Development, Bio-Electronics, Communication System, Signal processing, Biomedical Signal and Image Processing, Machine Learning, Wireless Communication, Massive MIMO, WSN, High Power Microwave Devices |

Above eligibility is minimum requirement only. A good academic career and higher qualifications will be preferred, and mere eligibility shall not ensure a call for admission test and/or viva voce.



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**Total numbers of seats in this scheme are 02 (Two) under Full Time Category and 01 (One) Under Part Time Category**, the distribution of seats will be based on qualification, expertise, performance in admission test/viva and suitability of the applicants.

Under the “Visvesvaraya PhD scheme for Electronics and IT: Phase-II” it is essential to work on research problems having relevance to Industry. The admission calendar, fee structure, academic regulations will be as per regular PhD admission process of the institute. For details of PhD regulation of NERIST visit <https://www.nerist.ac.in/> and for more details on Visvesvaraya PhD scheme visit <https://phd.dic.gov.in/>.

### General Instructions and Information

- i. Relaxation for SC/ST/PWD Candidates for both Full Time and Sponsored (Part Time) as per applicable norms.
- ii. The date for determining the eligibility of candidates in every respect shall be considered as on the closing date, i.e., the last date of the submission of the online application form.
- iii. Depending upon the number of applications received, the Institute reserves the right to fix higher eligibility criteria for shortlisting of the candidates for the selection process.
- iv. Eligible candidates are required to appear for a written test and interview in respect of his/ her discipline to be conducted by the Department concerned. No TA/DA will be paid for attending test / interview.
- v. A GATE/NET qualified applicant in the relevant discipline/specialisation will be exempted from the Written Test for admission in Ph.D. However, s/he has to mandatorily appear in the interview conducted by the respective Department.
- vi. **A non-refundable and non-transferable application fee of ₹ 1500.00 (Rupees Fifteen Hundred only) along with the bank transaction charges is applicable for all candidates, irrespective of their categories.**
- vii. Candidates shall be governed as per PhD Regulations of NERIST (refer below link). [https://nerist.ac.in/wp-content/uploads/2024/12/Academic-ByeLaws-PG\\_PhD-Programmes.pdf](https://nerist.ac.in/wp-content/uploads/2024/12/Academic-ByeLaws-PG_PhD-Programmes.pdf)
- viii. The status regarding the selection of candidates for the admission to the Ph.D. Programme will be available on the Institute's website (<http://www.nerist.ac.in>). Selected candidates will be informed through email in respect of admission guidelines.
- ix. Hostel facilities for selected candidates will be allocated based on availability.
- x. Separate fee to be paid for separate applications.

### How to Apply:

1. The candidate has to download the application (<https://shorturl.at/IQRkk>) and fill-in all the details as sought in the form.
2. A non-refundable and non-transferable application fee of ₹ 1500.00 (Rupees Fifteen Hundred only) along with the bank transaction charges is applicable for all candidates, irrespective of their categories. The Details of Bank Account is as given:

**Name of the Bank Account: NERIST Academic Fee A/C**

**Name of the Bank: State Bank of India**

**Address of the Bank: NERIST Campus, P.O.-**

**Nirjuli, District: Papumpare, Arunachal Pradesh**

**IFSC: SBIN0018744; MICR Code: 791002008**



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3. The candidate must fill the Online Application form through (<https://forms.gle/tGhN4YJtih5SUjTN6>).
4. The Screenshot of the payment slip must be uploaded under 'payment details' section of the google form.
5. A hard copy of the application along with requisite documents should also be sent through registered/speed post at: **‘Nodal Officer, Visvesvaraya PhD scheme, NERIST, Nirjuli-791109, Arunachal Pradesh’.**
6. If a candidate is applying for more than one category (full time or part time), the candidate must pay application fees separately and choose the desired departments/category in the online application form.
7. Candidates are advised to check the application in all aspects before pressing the submit button. Once the application is submitted, it cannot be modified. Regarding any issue on filling up online form, please email : [phd.visvesvaraya@nerist.ac.in](mailto:phd.visvesvaraya@nerist.ac.in)
8. Incomplete application, application received without Payment Receipt or without the academic documents as stated below shall be summarily rejected without any explanation.
9. The document should be properly oriented and arranged in the order given below:
  - a. Filled in application form
  - b. Application fee receipt
  - c. Proof of date of birth
  - d. Educational qualifications, mark sheets and certificates in line with the filled online application form.
  - d. Caste certificate (SC/ST/OBC) in standard format issued by the Competent Authority. OBC certificate should be latest.
  - e. Sponsorship Certificate/No Objection Certificate from the employers (if employed) in the prescribed format;  
[https://nerist.ac.in/wp-content/uploads/2025/03/Information-Brochure-PG-PhD-2025-26\\_NEPGT-2025\\_26.pdf](https://nerist.ac.in/wp-content/uploads/2025/03/Information-Brochure-PG-PhD-2025-26_NEPGT-2025_26.pdf)
  - f. Self-attested copy (1<sup>st</sup> page) of all research publications in Journals and Conferences (if any).
  - g. Disability certificate in standard format (in case of PWD candidates) issued by the Competent Authority.
  - h. One government issued ID proof (EPIC, PAN, AADHAR etc).

### Important Dates (Tentative):

|   |                            |
|---|----------------------------|
| Date of admission notification                          | 26 <sup>th</sup> May 2025  |
| Start date of online application                        | 27 <sup>th</sup> May 2025  |
| Last date of filling online application (up to 5:00 PM) | 30 <sup>th</sup> June 2025 |
| Last date of receiving of hard copy of the application  | 7 <sup>th</sup> July 2025  |



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|  |                            |
|--|----------------------------|
| Notification of Shortlisted Candidates     | 10 <sup>th</sup> July 2025 |
| Written Test and Interview                 | 24 <sup>th</sup> July 2025 |
| Display of Selected Candidates & Admission | 25 <sup>th</sup> July 2025 |
| Admission to selected Candidates           | To be notified later       |

No admit card will be sent and all information related to admission shall be communicated through the website. Visit **[www.nerist.ac.in](http://www.nerist.ac.in)** regularly for announcements related to PhD admission. The admission committee is fully responsible for the selection and their decision is final in the admission process.

## **For queries, you can contact below:**

Contact: +91-8415912663

Email: [nodal.visvesvaraya@nerist.ac.in](mailto:nodal.visvesvaraya@nerist.ac.in)

Sd/-

Dr. Kurmendra

Nodal Officer, Visvesvaraya PhD  
scheme, NERIST, Nirjuli



**North Eastern Regional Institute of Science and Technology**  
(Deemed to be University u/s 3 of the UGC Act, 1956 Under the Ministry of Education, Govt. of India)

**PhD Admission under "Visvesvaraya PhD Scheme for Electronics and IT:  
Phase II" Sponsored by MeitY, Govt. of India,  
Academic Year: 2025 - 26**

**Application form for Full-time / Part-time PhD under "Visvesvaraya PhD scheme: Phase II**

Paste your  
Recent  
Photograph

1. Advertisement Reference Number:
2. Name of the Department:
3. Name of the Candidate (BLOCK LETTER):
4. Father's Name (BLOCK LETTER):
5. Mother's Name (BLOCK LETTER):
6. (a) Date of Birth:      (b) Sex (Male/Female):  
(c) Marital Status: Married/ Single      (d) Category: SC/ST/OBC/Open
7. Applied category (Tick anyone):      (a) Full Time      (b) Part Time
8. Address for Communication (BLOCK LETTER)

Telephone/ Mobile No.

E-mail ID:

9. Previous research experience:
10. Details of Publication/Patent/Projects, if any (Attach additional sheet if needed):
11. GATE/NET (if any): Qualified (Yes/No):      Discipline: \_\_\_\_\_  
Score: \_\_\_\_\_  
Rank: \_\_\_\_\_  
Year: \_\_\_\_\_



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**PhD Admission under "Visvesvaraya PhD Scheme for Electronics and IT:  
Phase II" Sponsored by MeitY, Govt. of India,  
Academic Year: 2025 - 26**

12. Academic Qualification:

| Name of the Exam Passed | Name of the School/College/Institute/University | Year of Passing | Discipline / Specialization | % Marks/ CGPA |
|-------------------------|---|-----------------|-----------------------------|---------------|
| 10 <sup>th</sup>        |   |                 |                             |               |
| 12 <sup>th</sup>        |   |                 |                             |               |
| Diploma                 |   |                 |                             |               |
| B.E / B.Tech            |   |                 |                             |               |
| M.E /M.Tech             |   |                 |                             |               |
| Others (if any)         |   |                 |                             |               |

13. Name of the employer, if applicable, & total experience:

I do hereby declare that the information furnished in this application is true to the best of my knowledge and belief. If selected, I promise to abide by the rules and regulations of the Institute.

Date:

Place:

[Attach additional sheets, if needed]

Full Signature of the Applicant

**Annexure - III**

**Format of Sponsorship Certificate for Ph.D. program for employed persons  
(This should be typed on the letter head of the sponsoring organization)**

Reference No. \_\_\_\_\_

Date: \_\_\_\_\_

To

The Director  
NERIST  
Nirjuli – 791 109  
Arunachal Pradesh

**Sub: Sponsorship Certificate.**

Sir/Madam,

Mr./Ms. \_\_\_\_\_ who has been working in this organization  
as \_\_\_\_\_ is hereby sponsored for admission into the Ph.D.  
programme at NERIST, from the session starting on \_\_\_\_\_ as a  
full time student.

The employee will be relieved from his/her duties in the organization to join the Ph.D.  
programme of NERIST.

Date:

Place:

Signature:

Name:

Designation:

**Seal and Signature of sponsoring authority.**



**Annexure - IV**

**Format of No-Objection Certificate for Ph.D. (Full Time/Part time) program for employed persons  
(This should be typed on the letter head of the sponsoring organization)**

Reference No. \_\_\_\_\_

Date: \_\_\_\_\_

To

The Director  
NERIST  
Nirjuli – 791 109  
Arunachal Pradesh

**Sub: No objection certificate**

Sir/Madam,

It is hereby certified that Mr./Ms. \_\_\_\_\_ has been working  
in this organization as \_\_\_\_\_.

This organization has no objection to his/her being admitted to the Ph.D. programme at  
NERIST, Nirjuli, from the session starting from \_\_\_\_\_ as a Full Time/Part Time  
student.

Date:  
Place:

Signature:  
Name:  
Designation:

**Seal and Signature of sponsoring authority.**

**Format for No-Objection Certificate from Principal Investigator for project fellows  
(This should be typed on the letter head of the sponsoring organization)**

Reference No. \_\_\_\_\_

Date: \_\_\_\_\_

To

The Director  
NERIST  
Nirjuli – 791 109  
Arunachal Pradesh

**Sub: No objection certificate.**

Sir/Madam,

Certified that Mr./Ms. \_\_\_\_\_, working as  
a \_\_\_\_\_ in the project (title of the Project) \_\_\_\_\_  
in the Department/Centre \_\_\_\_\_ funded by \_\_\_\_\_ since \_\_\_\_\_.  
I have no objection in allowing him/her to join the Ph.D. programme at NERIST under the category  
of project staff.

Further, he/she will be allowed to carry out his/her course work/research work of the Ph.D.  
programme without hampering the project work.

Date:  
Place:

Signature:  
Name:  
Designation:

**Seal and Signature of sponsoring authority.**

## Annexure - VI

### SYLLABI FOR Ph.D. ENTRANCE TEST

|  |   |
|--|---|
| <b>SECTION A: RESEARCH METHODOLOGY</b><br>(For all subjects under Science/Engineering/Humanities/Social Sciences/Management) |   |
| Unit-I   | Research problem; Research objectives; Research methods, Types of research: Descriptive, Analytical, Applied, Fundamental, Quantitative, Qualitative, Conceptual and Empirical.   |
| Unit-II  | Literature review: primary, secondary, e-resources, database sources; Formulation of Hypotheses; Defining and formulating the research problems.  |
| Unit-III   | Research design and methods: Randomize block design, Complete randomize design and Latin square design; Developing a research plan; Determining experimental and sample designs; Sampling error.  |
| Unit-IV  | Data Collection and analysis; Data Analysis with Statistical Packages - Hypothesis-testing - Generalization and Interpretation; Computer Processing.  |
| Unit-V   | Statistical Analysis: Introduction to Statistics – Probability Theories – Conditional Probability, Poisson Distribution, Binomial Distribution and Properties of Normal Distributions – Hypothesis Tests – One Sample Test – Two Sample Tests/Chi-Square Test, Association of Attributes - Standard Deviation – Co-Efficient of Variations. Statistical Applications – Correlation and Regression Analysis – Analysis of Variance – Partial and Multiple Correlation. |
| Unit-VI  | Reporting and thesis writing; Types of report; Report writing; Bibliography; Referencing; Presentation; Importance of effective communication; Copy right; Intellectual Property Right and Patent Law; Plagiarism.  |
| <b>SECTION B:</b><br>(Syllabus of National Level Tests like GATE/NET for EC/CS subjects)                                     |   |